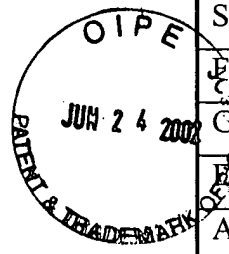


06-25-02

AF/2800



First Named Inventor	D. Mark Durcan	TRANSMITTAL FORM (LARGE ENTITY)
Serial No.	09/652,998	
Filing Date	August 31, 2000	
Group Art Unit	2814	
Examiner Name	Anh D. Mai	
Attorney Docket No.	400.153US07	
Title: CONTAINER CAPACITOR STRUCTURE AND METHOD OF FORMATION THEREOF		

Commissioner for Patents
Box AF
Washington, D.C. 20231

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JUN 26 2002
TC 2800 MAIL ROOM

Enclosures
The following documents are enclosed:
<input checked="" type="checkbox"/> A Response to Final Office Action (6 pgs. plus 2 pgs. Marked Up Version)
<input checked="" type="checkbox"/> A return postcard.
Please charge any additional fees or credit any overpayments to deposit account 501373.
CUSTOMER NUMBER: 27073

Submitted By					
Name	Russell D. Slifer	Reg. No.	39,838	Telephone	(612) 312-2202
Signature				Date	6/24/02

Fogg Slifer Polglaze Leffert & Jay, P. A.; P. O. Box 581009, Minneapolis, Minnesota 55458-1009

CUSTOMER NUMBER: 27073

Certificate of Mailing
Express Mail" mailing label number: EV075564266US Date of Deposit: June 24, 2002
These papers and fees are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and addressed to the Commissioner for Patents, Washington, D.C. 20231

(LARGE ENTITY TRANSMITTAL UNDER 37 C.F.R. 1.10)

S/N 09/652,998



PATENT

#12/CHE
7/16/2
Surler

AMENDMENT UNDER 35 U.S.C. § 1.116-- EXPEDITED EXAMINATION PROCEDURE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor:	D. Mark Durcan	Examiner:	Anh D. Mai
Serial No.:	09/652,998 ✓	Group Art Unit:	2814
Filed:	August 31, 2000	Docket:	400.153US07
Title:	CONTAINER CAPACITOR STRUCTURE AND METHOD OF FORMATION THEREOF		

N. Brunson
8/28/02

AMENDMENT AND RESPONSE UNDER 35 U.S.C. § 1.116

Commissioner for Patents
BOX AF
Washington, D.C. 20231

In response to the Final Office Action mailed April 22, 2002, please consider the following arguments:

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IN THE CLAIMS

Please amend the following claims:

13. An array of capacitors, comprising:
- a first bottom capacitor plate;
 - a second bottom capacitor plate;
 - a third bottom capacitor plate;
 - a contact between said first bottom capacitor plate and said second bottom capacitor plate, the contact downwardly extends from a vertical height above a top of the first bottom capacitor plate;
 - a trench between said second bottom capacitor plate and said third bottom capacitor plate;
 - a common top capacitor plate over said first bottom capacitor plate, said second bottom capacitor plate, and said third bottom capacitor plate, wherein said top capacitor plate extends toward said contact at a first level within said array, the top capacitor plate includes a lateral